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Г	APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.	٦
	09/476,092	01/03/2000	DAVID F. SORRELLS	1744.0250001	7304	
	26111 7	7590 07/25/2006		EXAMINER		
	•	ESSLER, GOLDSTEI ORK AVENUE, N.W.	MEHRPOUR, NAGHMEH			
		N, DC 20005		ART UNIT	PAPER NUMBER	
		•		2617		_

DATE MAILED: 07/25/2006

Please find below and/or attached an Office communication concerning this application or proceeding.

		Application	on No.	Applicant(s)					
			92	SORRELLS ET AL.					
Office Action Summary		Examiner		Art Unit					
			Mehrpour	2617					
Period fo	The MAILING DATE of this communication or Reply	appears on the	cover sheet with the c	orrespondence ad	ddress				
A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION. - Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filled after SIX (6) MONTHS from the mailing date of this communication. - If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication. - Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filled, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).									
Status									
1)	Responsive to communication(s) filed on 2	7 April 2006.							
2a)⊠		This action is n	on-final.						
3)[Since this application is in condition for allo	wance except	for formal matters, pro	secution as to the	e merits is				
	closed in accordance with the practice under Ex parte Quayle, 1935 C.D. 11, 453 O.G. 213.								
Disposit	ion of Claims								
4)⊠	Claim(s) 1-22 and 25-28 is/are pending in t	he application							
	4a) Of the above claim(s) is/are withdrawn from consideration.								
5)🖂	5) Claim(s) 6-18 is/are allowed.								
6)⊠	Claim(s) <u>1-5, 19-28</u> is/are rejected.								
7)	Claim(s) is/are objected to.								
8)□	Claim(s) are subject to restriction an	nd/or election r	equirement.						
Applicat	ion Papers								
9)[The specification is objected to by the Exam	niner.							
10)	The drawing(s) filed on is/are: a)☐ a	accepted or b)	objected to by the E	Examiner.					
	Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).								
	Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).								
11)	11)☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.								
Priority (under 35 U.S.C. § 119								
	12) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f). a) All b) Some * c) None of:								
	1. Certified copies of the priority docum	ents have bee	n received.						
	2. Certified copies of the priority docum		• •						
	3. Copies of the certified copies of the p	-		ed in this National	Stage				
	application from the International Bureau (PCT Rule 17.2(a)).								
* See the attached detailed Office action for a list of the certified copies not received.									
Attachmen	• •								
	e of References Cited (PTO-892) e of Draftsperson's Patent Drawing Review (PTO-948)		4) Interview Summary Paper No(s)/Mail Da						
3) 🔯 Infori	mation Disclosure Statement(s) (PTO-1449 or PTO/SB/ r No(s)/Mail Date		5) Notice of Informal P 6) Other:		O-152)				

DETAILED ACTION

Information Disclosure Statement

1. The information disclosure statement filed reference listed in the information Disclosure Submitted on 05/08/06 have been considered by the examiner (see attached PTO-1449

Claim Rejections - 35 USC > 102

The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

- (b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.
- 2. Claims 1-5, 19-28, are rejected under 35 U.S.C. 102(b) as being anticipated by Shimamura et al. (US Patent Number 4,143,322).

Regarding Claims 1, 26, Shimamura teaches method for down-converting a frequency modulated (FM) signal, comprising:

- (1) aliasing the FM signal at an aliasing rate, said aliasing rate being determined by the frequency of the FM signal (col 3 lines 58-67, col 4 lines 1-39);
- (2) adjusting said aliasing rate to compensate for frequency changes of the FM signal (col 3 lines 58-67, col 4 lines 1-39); and
- (3) outputting, responsive to steps (1) and (2), a demodulated baseband information signal (col 3 lines 58-67, col 4 lines 1-39);

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wherein the aliasing rate is based upon the demodulated baseband information signal (col 3 lines 58-67, col 4 lines 1-39).

Regarding Claim 2, Shimamura inherently teaches method wherein step (1), comprising:

Aliasing the FM signal at an aliasing rate that is substantially equal to a sub-harmonic of a frequency of the FM signal (642).

Regarding Claim 3, Shimamura teaches a method wherein step (1) comprises:

Aliasing the FM at an aliasing rate that is substantially equal to a frequency (col 3 lines 45-67, col 4 lines 1-29).

Regarding Claim 4, Shimamura teaches a method further comprising the step of compensating for phase delays to maintain bandwidth and stability (col 3lines 45-67, col 4 lines 1-17).

Regarding Claim 5, Shimamura teaches a method wherein the FM signal has a frequency substantially equal to a family radio frequency (col 3 lines 45-67, col 4 lines 1-28)

Regarding Claim 19, Shimamura teaches a method for directly down converting frequency modulated (FM) signal having a carrier frequency, comprising the steps of:

(1) aliasing the FM signal with a first local oscillator (LO) signal to create a first down-converted signal, said first LO signal having a first LO frequency and a first LO phase (col 3 lines 6-67, col 4 lines 1-29);

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(2) aliasing the FM signal with a second signal to create a second down-converted signal, said second LO frequency and a second wherein said second LO frequency is substantially the same as said first LO frequency, and wherein said second LO phase is shifted relative to said first

LO phase (col 3 lines 6-67, col 4 lines 1-39);

(3) generating a control signal from the first and second down-converted signals,

wherein the first and second LO signals are generated from the control signal

(col 3 lines 5-44); and

(4) adjusting the control signal based on frequency changes of the FM signal (col 3 lines

44-58).

Regarding Claims 20, 27, Shimamura teaches a method wherein step (3) comprising:

(a) summing the first and second down-converted signals to generate a summation signal (col 3

lines 6-44)1; and

(b) integrating the summation signal to generate the control signal wherein the first and second

LO signals are generated from the control signal (col 3 lines 4-44).

Regarding Claim 21, Shimamura teaches a method wherein step (4) comprises the step of:

adjusting said control signal to maintain said mixer signal at a value substantially equal to zero

(col 3 lines 6-57).

Regarding Claims 22, 28, Shimamura teaches a method wherein comprises:

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Maintaining the first second LO signals such that one of the first and second LO signals leads the FM signal, another of the first and second LO signals lags the FM signal (col 3 lines 6-67, col 4 lines 1-60).

Allowable Subject Matter

3. Claims 6-18, are allowed.

The followings a statement of reasons for the indication of allowable subject matter:

Regarding claim 12, the present application teaches a first aliasing module and second aliasing module to alias the FM signal and a summing module to combine the first signals and the second signal to create a summation signal as specifically mentioned in claim 12.

Regarding claim 6, the present application teaches a down converter comprising: a first aliasing module; and a second aliasing module; and a summer coupled to the first and second aliasing modules as mentioned in claim 26.

The closest record of prior art Shimamura teaches an FM receiver comprising a local provides a radio receiver for receiving a selected FM signal in which a message signal A(t) modulates the frequency of carrier signal, comprising a local oscillator for producing a local oscillator signal, a heterodyning stage for heterodyning said selected FM signal with said local oscillator signal so as to reduce the frequency which is modulated by said message signal to an intermediate frequency, and a demodulator stage for accepting an output signal from said heterodyning stage and extracting said message signal therefrom, characterized in that said heterodyning stage comprises signal producing means for producing n signals a sub.1.. a sub.n wherein each signal

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consists of said message signal modulated at said intermediate frequency, said signals a.sub.1. a.sub.n being related by the expression. Shimamura fails to teach a first aliasing module and second aliasing module to alias the FM signal and a summing module to combine the first signals and the second signal to create a tracking module changes in frequency of the FM signal, an LO signal changing module to change the first and second LO signals based on the changes in frequency.

Response to Arguments

4. Applicant's arguments with respect to claims 1-5, 19-28, have been considered but are most in view of the new ground(s) of rejection.

Conclusion

5. Any responses to this action should be mailed to:

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Naghmeh Mehrpour whose telephone number is 571-272-7913. The examiner can normally be reached on 8:00-6:00.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Nick Corsaro be reached (571) 272-7876.

The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

NM

July 6, 2006

MELODY MEHRPOUR: PATENT EXAMINER